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From:

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RE: 10/722747

OR (P-17968)(Intel)

To:

MR. SCOTT CHRISTENSEN

Organization: PTO

Tel.:

571-270-1144 & Fax: 571-270-2144

Date:

9 Feb 09

Number of Pages (including the cover page): 3

Note:

Hi Scott,
Here is our amendment for our interview, plus the PoA, from Intel. Please give us a time for personal interview (for you & your primary/SPE) (@ your office). We will discuss in more details at the interview. Anytime after (Feb-17-2009) is good for us. (Please call). Thanks for the previous tel. conversations & all your help. Regards- Bijan -
IP Consulting, Patent Search & Prosecution

PTO/SB/81 (01-06)

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Application Number	10/722,747
Filing Date	11-25-2003
First Named Inventor	Charles E. Narad
Title	
Art Unit	2444
Examiner Name	CHRISTENSEN, SCOTT B.
Attorney Docket Number	P17968

I hereby revoke all previous powers of attorney given in the above-identified application.

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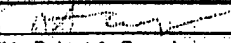


Applicant/Inventor.



Assignee of record of the entire interest. See 37 CFR 3.71.
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)

SIGNATURE of Applicant or Assignee of Record

Signature		Date	NOV-16-2008
Name	Mr. Robert A. Greenberg	Telephone	
Title and Company	Attorney or Representative of Intel Corp, the assignee of the patent		

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.



*Total of 1 forms are submitted.

This collection of information is required by 37 CFR 1.31, 1.32 and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Attorney Docket: 42. PI7968
Serial No.: 10/1722,747

Proposed amended Claim 1 below is a combination of the latest amended Claims 1, 10, 13 (original) and 14. We think that it is not taught by the prior art.

1. A network interface, comprising:

a direct memory access unit; and

circuitry to:

receive and transmit network data;

maintain a set of statistics metering operation of the network interface, the set of statistics including at least one selected from the group of: (1) a number of bytes received, and (2) a number of packets received;

receive a periodic time value to perform periodic direct memory access transfers of the maintained set of statistics to a host processor memory; [[and]]

periodically initiate a direct memory access transfer of the set of statistics to the host processor memory at a periodicity of the periodic time value [[.]]; and

configure said initiation of the direct memory access transfers by intercepting packets received from the host traveling along a transmit path and determining configuration information from the received packets.